

FIG. 1

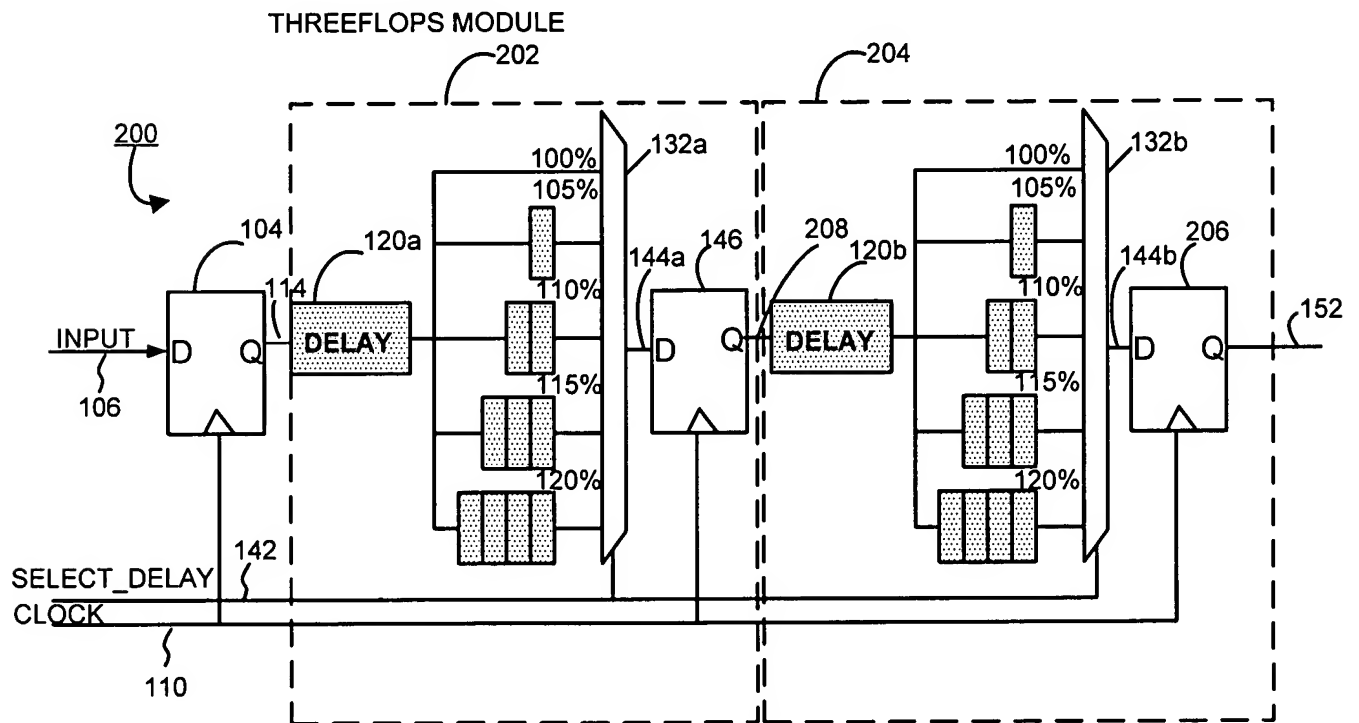
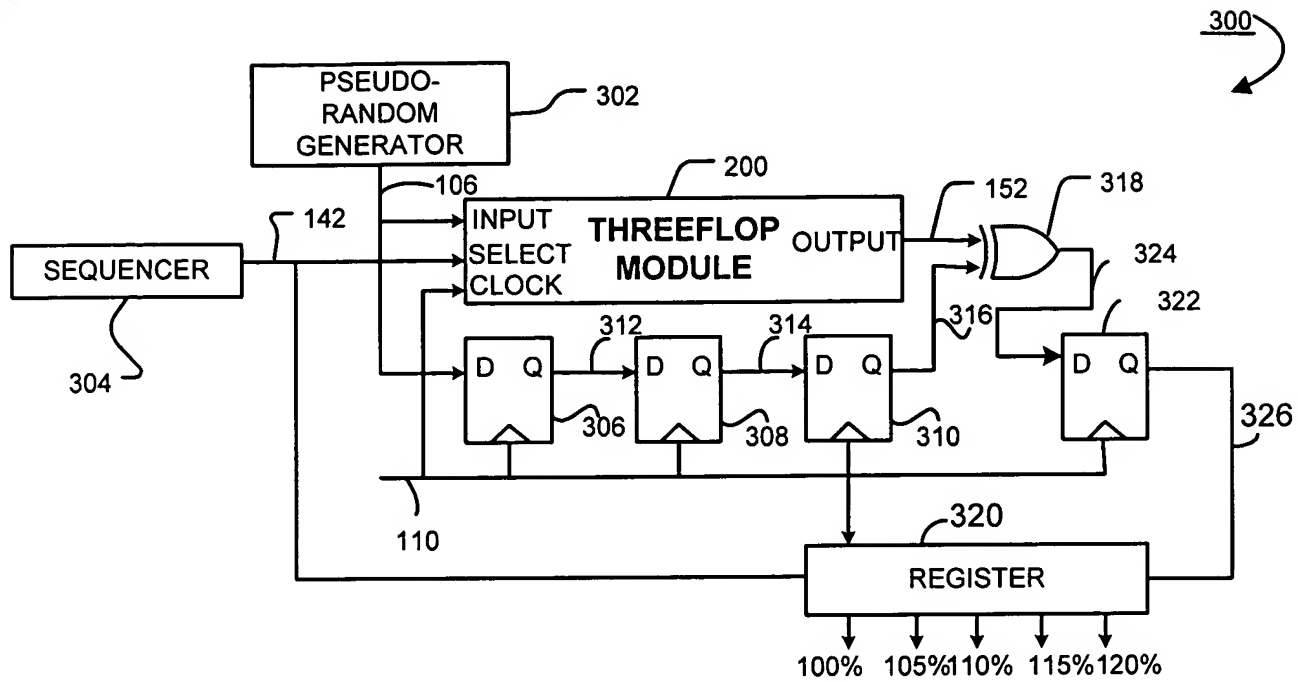
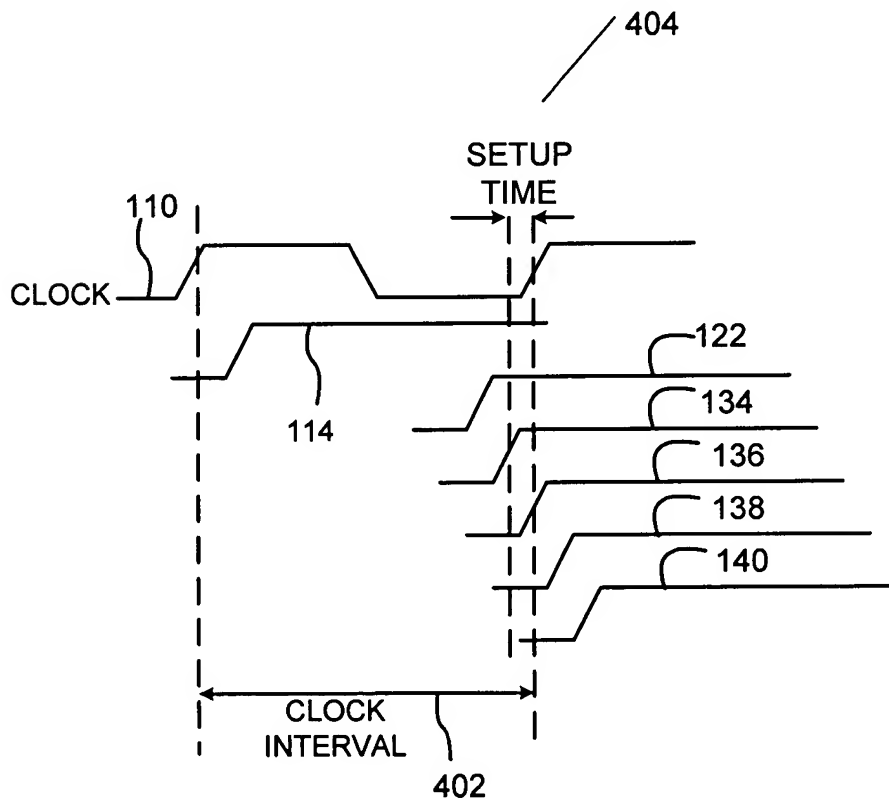


FIG. 2

**METHOD AND APPARATUS FOR DETERMINING A  
PROCESSING SPEED OF AN INTEGRATED CIRCUIT**  
Inventor: Greg Sadowski; Atty. Docket No. 00100.03.0011



**FIG. 3**



**FIG. 4**

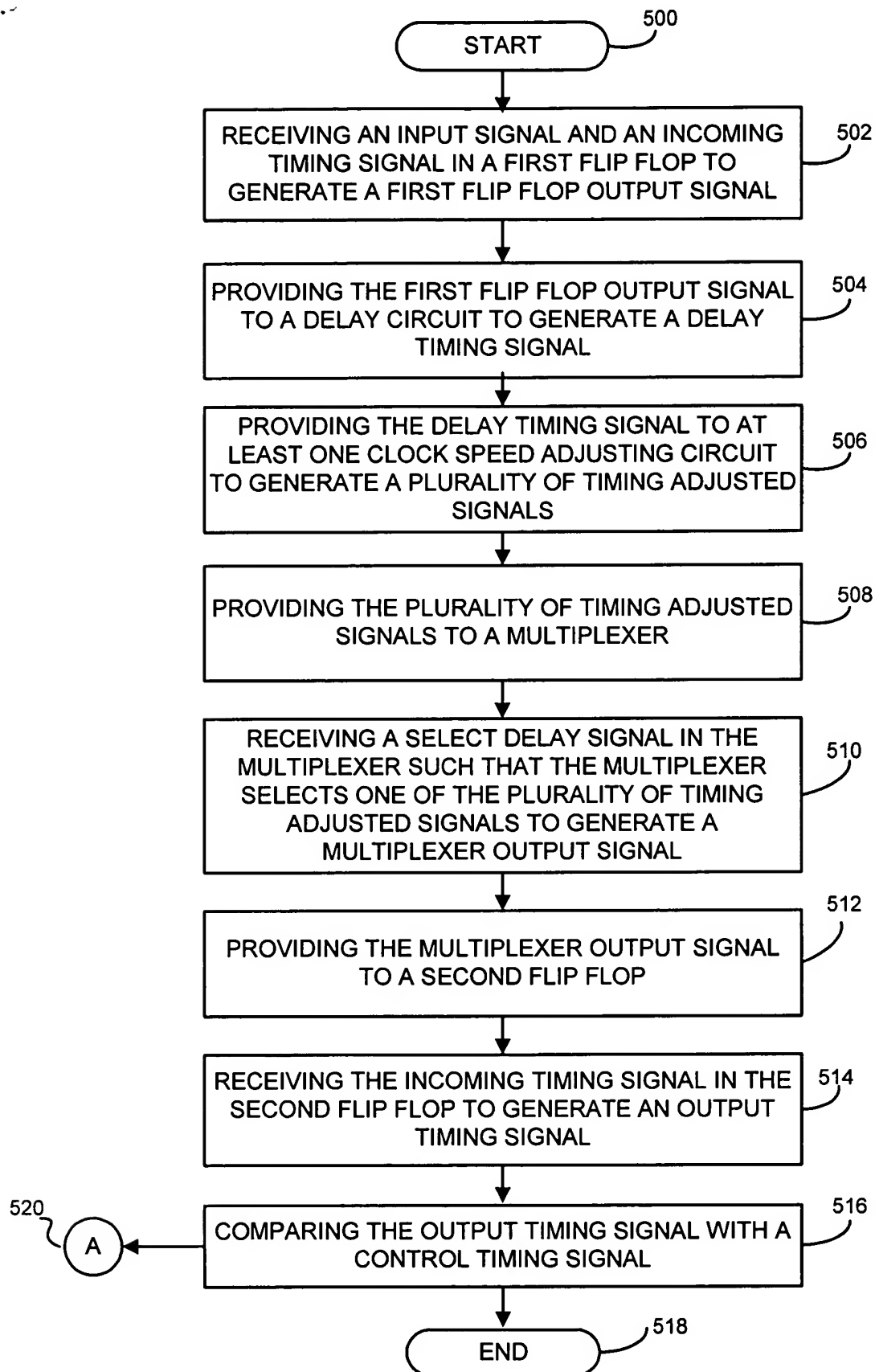


FIG. 5

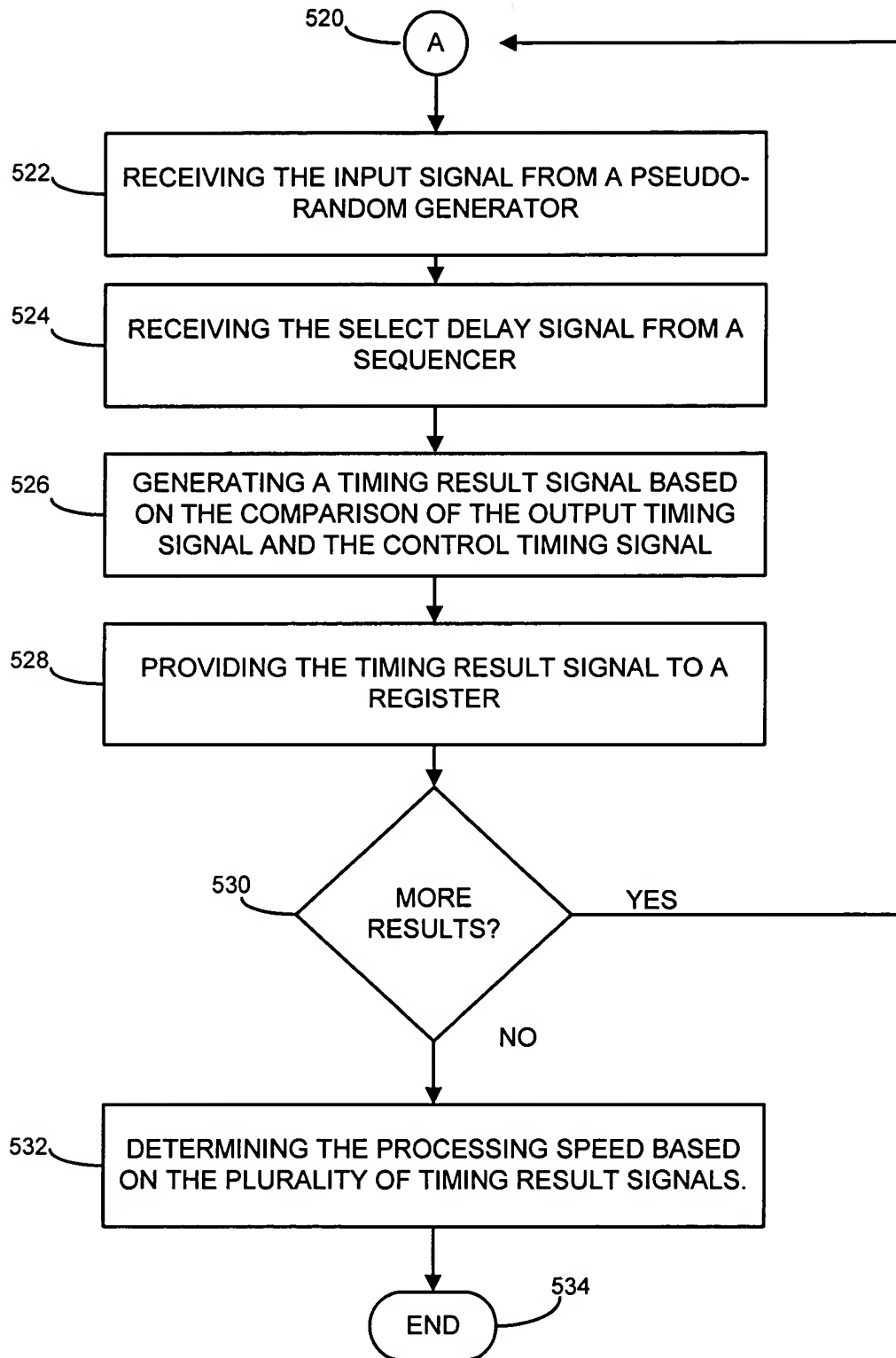


FIG. 6

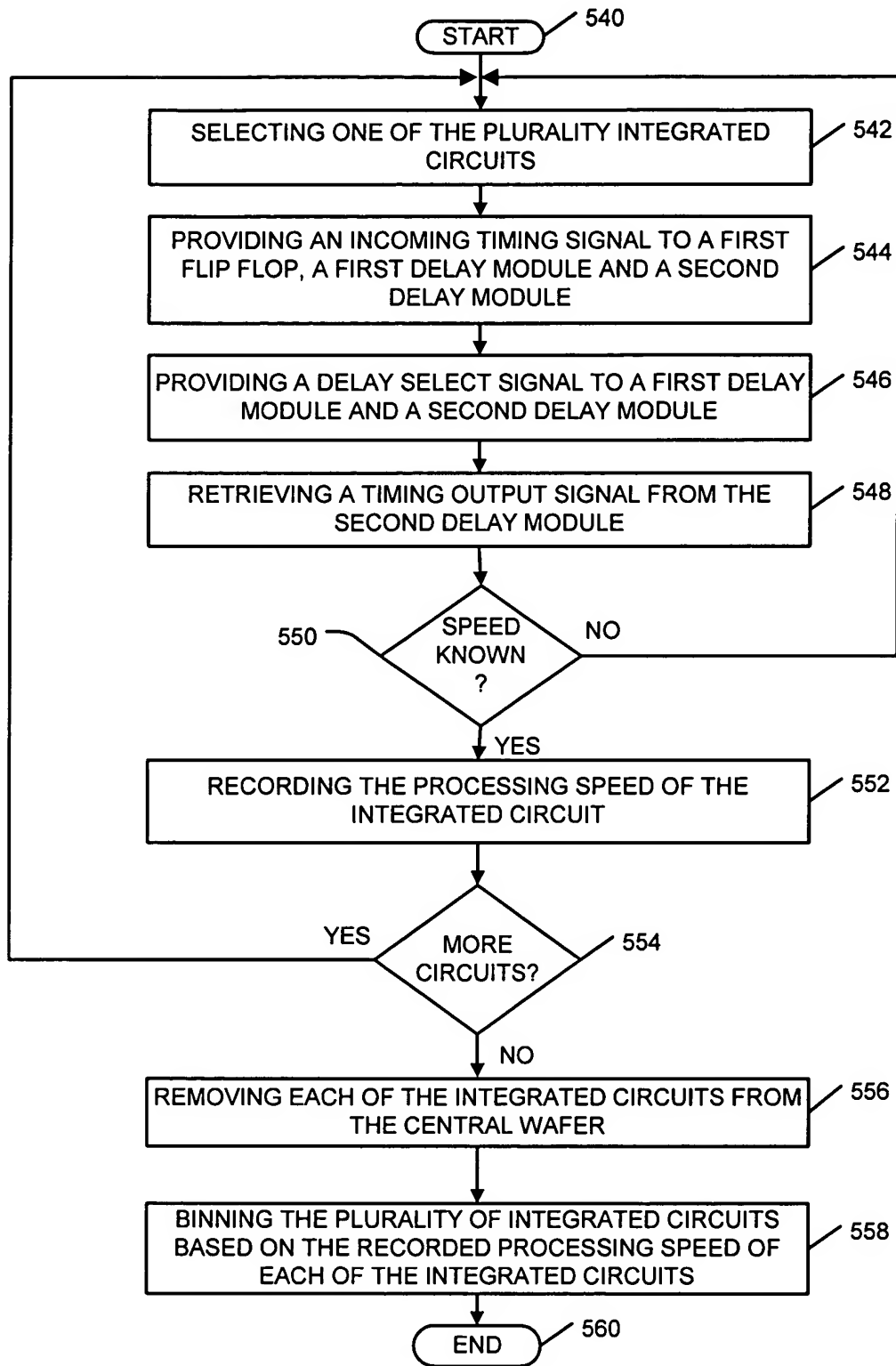


FIG. 7